

[illegible]

TI-30105

Jay B. Reimer, et al.

Serial No:

Filed: November 8, 2001


For: Multicore DSP Device Having Coupled Subsystem Memory Buses for Global DMA Access

**PRELIMINARY AMENDMENT**

Ass't Commissioner for Patents  
Washington, DC 20231

Dear Sir:

**EXPRESS MAILING"** Mailing Label No **EL645453245**. Date of Deposit: **November 8, 2001**. I hereby certify that this paper is being deposited with the U.S. Postal Service Express Mail Post Office to Addressee Service under 37 CFR 1.10 on the date shown above and is addressed to: Ass't Commissioner for Patents, Washington, D.C. 20231.

  
Robin E. Barnum

Please amend the specification by inserting before the first line, the following sentence:

--This application claims priority under 35 USC §119(e)(1) of Provisional Application Number 60/249,083, filed November 15, 2000.--

Respectfully submitted,

Went L Marshall

Robert D. Marshall, Jr.  
Attorney for Applicant(s)  
Reg. No. 28,527

Texas Instruments Incorporated  
P.O. Box 655474, MS 3999  
Dallas, TX 75265  
(972) 917-5290